

NEWS RELEASE

For more information, contact:

Nanette Collins
Public Relations for Verific
(617) 437-1822
nanette@nvc.com

Rick Carlson Joins Verific Design Automation as Vice President of Worldwide Sales *Well-Known EDA Industry Sales Veteran Will Oversee All Sales Activities*

Alameda, Calif. — November 18, 2003 — Verific Design Automation, the leading provider of Verilog and VHDL front-ends for electronic design automation (EDA) applications, today announced the appointment of EDA industry sales veteran Rick Carlson as vice president of worldwide sales.

In this newly created role, Carlson will oversee all of Verific's sales activities and reports to its VP of operations Michiel Ligthart. He is based in Durango, Colo.

Carlson, a 23-year EDA veteran of the electronic design automation (EDA) industry joins Verific from AccelChip, where he held a similar position. Prior to AccelChip, Carlson held positions as vice president of sales for Averant, Synplicity, Inc. (Nasdaq: SYNP), Escalade, now part of Mentor Graphics Corp. (Nasdaq: Ment), and EDA Systems. Carlson also founded the EDA trade organization the EDA Consortium in 1987. He holds a bachelor's degree in Mathematics from Illinois Institute of Technology (IIT) in Chicago.

“We are delighted to have someone of Rick’s talent and experience join our executive team,” notes Rob Dekker, president and founder of Verific. “His knowledge of the EDA industry coupled with his experience in synthesis and the HDL language space will be a great asset as we continue to develop and market our HDL language synthesis solutions.”

Adds Carlson: “The industry’s growth over the last 10 years has been a result of having key infrastructure components available to jumpstart new products from startup companies as well as more mature EDA and semiconductor companies. Verific’s technology has allowed its customers to accelerate their product development and get higher quality products to market much sooner as a result. I’m excited to join the excellent team at Verific and look forward to expanding its market opportunity.”

About Verific Design Automation

Verific Design Automation was founded in 1998 by electronic design automation (EDA) industry veteran Rob Dekker. It develops and sells C++ source code Verilog and VHDL front ends — parsers, analyzers and elaborators — as well as a generic hierarchical netlist database for EDA applications. Verific’s technology has been licensed in many applications, combined shipping more than 20,000 end-user copies. Corporate headquarters is located at: 1516 Oak Street, Suite 115, Alameda, Calif. 94501. Telephone: (510) 522-1555. Facsimile number: (510) 522-1553. Email: info@verific.com. Website: <http://www.verific.com>.

— end —

Verific Design Automation acknowledges trademarks or registered trademarks of other organizations for their respective products and services.