

NEWS RELEASE

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Sequence Selects Verific HDL Component Software

Industry Leading Power, Signal Integrity Expert Notes Verific's Fast Compile Times

Alameda and Santa Clara, Calif. — May 03, 2004 — Sequence Design, Inc.,

today announced that it has integrated Hardware Description Language (HDL)

Component Software from Verific Design Automation with its industry leading low-power design and analysis software.

Verific's HDL Component Software — C++ source code-based Verilog and VHDL parsers, analyzers and elaborators — acts as the front end to Sequence's PowerTheater™ RTL power estimation software for managing power early in the design process.

Says Tom Miller, Sequence's vice president of engineering: "The speed of the Verific software is very impressive. We are seeing 10X throughput increases over the previous solution we used to manage mixed Verilog and VHDL designs. In addition we can now support all Verilog 2001 extensions. The software is robust and was easy to integrate with PowerTheater."

"Sequence is recognized as the leader in power and signal integrity software for nanometer design," adds Verific's President Rob Dekker. "It gives us great pride to be working with this innovative company."

About Verific Design Automation

Verific Design Automation was founded in 1998 by electronic design automation (EDA) industry veteran Rob Dekker. It develops and sells C++ source code-based Verilog and VHDL front ends — parsers, analyzers and elaborators — as well as a generic hierarchical netlist database for EDA applications. Verific's technology has been licensed in many applications, combined shipping more than 20,000 end-user copies. Corporate headquarters is located at: 1516 Oak Street, Suite 115, Alameda, Calif. 94501. Telephone: (510) 522-1555. Facsimile number: (510) 522-1553. Email: info@verific.com. Website: <http://www.verific.com>.

About Sequence

Sequence Design, Inc. enables system-on-chip designers to bring higher-performance and lower-power integrated circuits quickly to fabrication. Sequence's power and signal integrity software give its more than 120 customers the competitive advantage they need to excel in aggressive technology markets, despite demanding complexity and time-to-market issues of nanometer design.

Sequence has worldwide development and field service operations. The company was recently named by Reed Electronics as one of the top 10 companies to watch in the electronics industry. Sequence is privately held. Sequence is a member of Cadence Design Systems' Connections™ and Mentor Graphics' Open Door™ partnership programs. Additional information is available at sequencedesign.com.

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